

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Hiroshi YOSHIMURA et al.

Group Art Unit:

Serial No.:

Examiner:

Filed: August 16, 1999

For: A FLAT DISPLAY UNIT AND METHOD FABRICATING SAME

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to calculating the filing fee for and examination of the above-identified application,
please amend the claims as follows:

IN THE CLAIMS

3. (ONCE AMENDED) A flat display unit as set forth in claim 1 [or 2], wherein
said chassis includes fins provided on the second surface of the circuit board is mounted thereon,
in the form of a plurality of protrusions.

4. (ONCE AMENDED) A flat display unit as set forth in [claims 1 or 2] claim 1,
wherein said chassis has a through-hole formed adjacent to the periphery thereof, the
through-hole allowing a cable to pass therethrough for electrically interconnecting the display
panel and the circuit board.